

Docket No.: 21806-00113-US1
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re **Divisional** Patent Application of:
Arne W. Ballantine, et al.

Application No.: To Be Assigned

Group Art Unit: To Be Assigned

Filed: Concurrently Herewith

Examiner: To Be Assigned

For: METHOD OF REDUCING POLYSILICON
DEPLETION IN A POLYSILICON GATE
ELECTRODE BY DEPOSITING
POLYSILICON OF VARYING GRAIN SIZE

FIRST PRELIMINARY AMENDMENT

MS Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Prior to examination on the merits, please amend the above-identified U.S. patent application as follows:

Amendments to the Specification begin on page 2 of this paper;

Amendments to the Claims are reflected in the listing of claims which begins on page 3 of this paper; and

Remarks/Arguments begin on page 7 of this paper.